

31-Bit Mode - Assembler Programming.....	13:3
4095 - Limit.....	4:17

A

ACB: Access Method Control Block.....	10:2-3
Access Method.....	6:1
Add Instructions - A, AR, and AH.....	4:8
Addressability.....	1:9
Addressing.....	1:10
Addressing Modes.....	13:2
Alignment - Constants.....	2:5
Alignment - Boundary.....	2:9
AND Instructions.....	12:1
Arrays.....	9:2
Assembler Instructions.....	1:1
Assembler Language.....	1:1
Assembler Processing.....	1:4
Assembler Programming with 31-Bit Mode.....	13:3
Assembly and Link Editing.....	1:3

B

B Instruction.....	4:15
BAL: Branch and BALR: Link Instructions.....	8:10
BASSM - Branch and Save and Set Mode.....	13:3
BE Instruction.....	4:15
Bit Switches - Setting.....	12:4
Branch On Count Instruction - BCT and BCTR.....	4:19
Branching - BC and BCR Instructions.....	4:14
BSM - Branch and Set Mode.....	13:3
BXH: Branch On Index High Instruction.....	12:10
BXLE: Branch On Index Low or Equal Instruction.....	12:11

C

CALL Macro.....	8:7
CLC: Compare Logical Character.....	5:4
CLI: Compare Logical Immediate Instruction.....	5:5
CLM: Compare Logical Characters Under Mask Instruction.....	12:15
CLOSE Macro.....	6:6, 10:8
CLOSE Macros.....	10:7
CNOP Instruction.....	2:6
Codes Used by Instructions.....	4:11
Coding Conventions.....	1:16
Condition Codes.....	4:12
Conditional Processing.....	4:10
Control Sections.....	1:6
Conversion.....	7:3
CP Instruction.....	7:6
Cross-Reference Table.....	3:4
CVB Instruction.....	7:3
CVD Instruction.....	7:3

D

Data in Assembler Program	1:8
Data Types	2:1
DC Examples	2:4
DC Instruction	2:2
Debugging	11:1
Decimal Arithmetic Instructions	7:6
Defining Constants	2:2
Defining Literals	2:7
Defining Storage: DS Instruction	2:8
DELETE Macro	9:7
Diagnostics and Statistics	3:5
Divide Instructions - DR and D	4:22
DSECTs	1:6

E

ED - Edit and EDMK - Edit and Mark Instructions	7:8
ED Instruction	7:14
EDIT and MARK Instruction	7:18
ENDREQ Macro	10:13
Entry Point - Called Routine	8:12
EQU Instruction - Defining Symbols	2:10
ERASE Macro	10:13
Establishing Addressability	1:13
Examples: BASSM	13:4
Examples: BASSM and BSM	13:4
Examples: BSM	13:4
Exclusive OR Instructions	12:5
EXECUTE Instruction	12:9
Executing the Assembler	1:5
EXLST Macro	10:4
Extended Mnemonic Codes	4:16
External References and Entry Points	8:11
External Symbol Dictionary	3:1
EXTRN Statement	8:11

F

Feedback Field	10:14
Fill Byte	7:15

G

GET Macro	6:4, 10:12
GET, PUT, POINT, ENREQ, and ERASE Macros	10:12

I

IC: Insert Character Instruction 12:8
 ICM: Insert Character Under Mask Instruction..... 12:16
 Initializing - Arrays 9:8
 Initializing - Tables 9:8
 Initializing Tables and Arrays 9:8
 Inserting and Storing One Byte 12:8

L

L: Load Instruction..... 4:1
 LA: Load Address Instruction 4:17
 Link Editing..... 1:3
 Load Halfword Instruction 4:3
 Load Instructions 4:1
 LOAD Macro 9:6
 Load Multiple Instruction 4:4
 Location Counter 1:14
 Long Control Sections 1:25
 Long Control Sections and Multiple Base Registers..... 8:13
 LR Instruction 4:1
 LTR: Load and Test Register Instruction 4:7

M

Machine Instruction Formats..... 1:17
 Machine Instructions 1:1
 Macro and Copy Libraries 1:6
 Macro Instructions 1:2
 Message Bytes..... 7:9-10
 MODCB Macro 10:10
 MR: Multiply Instruction..... 4:20
 Multiply Decimal and Divide Decimal Instructions 7:6-7
 MVC Instruction - Moving Data 5:1-2
 MVI Instruction 5:3
 MVS and z/OS: System Architecture 13:1

N

Non- Exclusive OR Instructions 12:3

O

OPEN Macro 6:3, 10:7
 OPEN Macros 10:7
 OR Instructions 12:2
 OR Instructions - Non Exclusive 12:3

P

PACK Instruction	7:1
Padding	2:4
Parameter List	8:8
Passing Variable Number of Parameters	8:9
Pattern - Using Special Bytes	7:9
POINT Macro	10:12
Program Checks and Debugging	11:1
Program Relocation	11:4
PSW and Registers	11:2
PUT Macro	6:5, 10:12

R

Receiving Field in an Operation	1:17
Register Usage	8:6
Registers	1:12
Registers - Using	11:3
Relocability	1:9
Relocation Dictionary	3:4
Return Codes	10:14
RPL Macro	10:5-6
RR Format	1:18
RS Format	1:22
RX Format	1:20
RX: Format - Normal Way of Coding	1:21
RX Instruction Formats	9:1

S

Save Areas	8:2-4
Saving Registers	1:15
Shift Instructions - Shifting Data	4:21
ShowCB Macro and Feedback Field	10:14
SHOWCB Macro	10:11
SI Format	1:24
Significance - Start Byte Hex 21	7:12-13
Simple Selection	7:11
Source and Object Program	3:3
Special Use	4:17
SS Format	1:23
STC: Store Character Instruction	12:8
STCM: Store Character Under Mask	12:17
STH: Store Halfword Instruction	4:6
STM: Store Multiple Instruction	4:6
Store Instruction Formats	4:5
Store Instructions	4:5
Subfields	2:3
Subroutines	8:1
Subtract Instructions - SR, S, and SH	4:9
Symbols to Facilitate Coding	7:2, 7:4
Symbols	1:7

Tables.....	9:3-5
TCLOSE Macro.....	10:9
Test Under Mask Instruction.....	12:7
TM: Test Under Mask Instruction.....	12:7
TR: Translate Instruction.....	12:12
Translation.....	12:13
TRT Instruction.....	12:14
Truncation and Padding.....	2:4
V	
VSAM Files and Macros.....	10:1
V-type Constant.....	8:11
Z	
z/OS Considerations.....	8:5
z/OS File and DCB: Data Control Block.....	6:2
ZAP Instruction.....	7:5
Zero Suppression - Significance Off and On.....	7:16-17